

DOCKET NO.: TALW-0152

In Re Application of:

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	Tadayoshi Kachi, Junichi Takeuchi and Seiki Sakata				
Seria	l No.: Not yet assi	gned.	Group Art Unit: Not yet assigned.		
Filin	g Date: Herewith	l .	Examiner: Not yet assigned.		
For:	Power Converti	ng Method and A	Apparatus		
			EXPRESS MAIL LABEL NO: EL531171511US DATE OF DEPOSIT: October 17, 2000		
Box	Patent Applica	ation			
	☐ Provisional ☐	☐ Design			
	ant Commissioner to ngton DC 20231	for Patents			
Sir:					
	PATEN	NT APPLICATIO	ON TRANSMITTAL LETTER		
	Transmitted herev	vith for filing, plea	ase find		
\boxtimes	A Utility Patent A	pplication under 3	37 C.F.R. 1.53(b).		
	It is a continuing a	application, as foll	ows:		
	continuation/	divisional	ontinuation-in-part of prior application num	nber	
		~ ~	nder 37 C.F.R. 1.53(c). itted in duplicate).		
		11			

	,		
DOC	KET NO. : TALW-0152	- 2 -	PATENT
Includ	ding the following:		
	Provisional Application Cover She	eet.	
M	N D '- 1 Co. 1 Co. 1 Co. 1 co. 1 co. 1	L-1'	

	Provisional Application Cover Sheet.				
\boxtimes	New o	or Revise	ed Specification, including pages 1 to 28 containing:		
	\boxtimes	Specifi	cation		
	\boxtimes	Claims			
	\boxtimes	Abstrac	ct		
		Substit	ute Specification, including Claims and Abstract.		
			The present application is a continuation application of Application No filed The present application includes the Specification of the parent application which has been revised in accordance with the amendments filed in the parent application. Since none of those amendments incorporate new matter into the parent application, the present revised Specification also does not include new matter. The present application is a continuation application of Application No filed, which in turn is a continuation-in-part of		
			Application No		
	includ matter for suc	ing Spec has been th earlier	ier application Serial No Filed, effication, Claims and Abstract (pages 1 - @@), to which no new n added TOGETHER WITH a copy of the executed oath or declaration r application and all drawings and appendices. Such earlier application rporated into the present application by reference.		
	Please enter the following amendment to the Specification under the Cross-Reference to Related Applications section (or create such a section): "This Application:				

	Signed Statement attached deleting inventor(s) named in the prior application.				
	A Preliminary Amendment.				
\boxtimes	Five (5) Sheets of Formal Informal Drawings.				
	Petition to Accept Photographic Drawings.				
	☐ Petition Fee				
\boxtimes	An 🛮 Executed 🗆 Unexecuted Declaration or Oath and Power of Attorney.				
\boxtimes	An Associate Power of Attorney.				
	An \square Executed \square Copy of Executed Assignment of the Invention to				
	☐ A Recordation Form Cover Sheet.				
	☐ Recordation Fee - \$40.00.				
	The prior application is assigned of record to				
\boxtimes	Priority is claimed under 35 U.S.C. § 119 of Patent Application No11-312402				
	filed 11/02/99 in Japan (country).				
	A Certified Copy of each of the above applications for which priority is claimed: In the county In the co				
	An Executed or Copy of Executed Earlier Statement Claiming Small Entity Status under 37 C.F.R. 1.9 and 1.27 is enclosed. has been filed in prior application Serial No filed, said status is still proper and desired in present case.				
	Diskette Containing DNA/Amino Acid Sequence Information.				

DOC	KET N	O.: TALW-0152	- 4 -	PATENT
	Stater	nent to Support Submission	on of DNA/Amino A	cid Sequence Information.
	Appli 1.821 form: applic necess form: Listin	cation Serial Number (e), please use the firstiled in that application as action. It is understood the sary change in application that will be used for the iring is included in the or	t-filed, last-filed of the computer readable at the Patent and Trace number and filing danstant application. A riginally-filed specific	, is identical with that filed in In accordance with 37 CFR or only computer readable le form for the instant demark Office will make the ate for the computer readable paper copy of the Sequence ation of the instant application, ment for incorporation into the
\boxtimes	•	nation Disclosure Statemo		attached Form PTO-1449 are
	A cop	y of Petition for Extensio	on of Time as filed in	the prior case.
	Appei	nded Material as follows:		
\boxtimes	Retur	n Receipt Postcard (shoul	d be specifically item	ized).
	Other	as follows:		

FEE CALCULATION:

Cancel in this application	original claims	of the prior application before
calculating the filing fee.	(At least one original	independent claim must be retained
for filing purposes.)		

			SMALI	SMALL ENTITY		NOT SMALL ENTITY	
			RATE	FEE	RATE	FEE	
PROVISIONAL .	APPLICATION		\$75.00	\$	\$150.00	\$	
DESIGN APPLIC	CATION		\$160.00	\$	\$320.00	\$	
UTILITY APPLI	CATIONS BASE	FEE	\$355.00	\$	\$710.00	\$710.00	
UTILITY APPLICATION; ALL CLAIMS CALCULATED AFTER ENTRY OF ALL AMENDMENTS							
	No. Filed	No. Extra	******	******	******		
TOTAL CLAIMS	18- 20 =	0	\$9 each	\$	\$18 each	\$0	
INDEP. CLAIMS	9-3=	6	\$40 each	\$	\$80 each	\$480.00	
TOTAL CLAIMS 18-20 = 0 INDEP. CLAIMS 9-3 = 6 CLAIMS FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM			\$135	\$	\$270	\$0	
ADDITIONAL FILING FEE				\$		\$0	
TOTAL FILING FEE DUE				\$		\$1,190.00	

\boxtimes	A Check is	enclosed i	n the amount	of \$1,190.00 .
-------------	------------	------------	--------------	------------------------

The Commissioner is authorized to charge	payment of the following fees and to
refund any overpayment associated with the of this application to deposit account 23-30	
11	1

	The foregoing	g amount due.
--	---------------	---------------

- Any additional filing fees required, including fees for the presentation of extra claims under 37 C.F.R. 1.16.
- Any additional patent application processing fees under 37 C.F.R. 1.17 or 1.20(d).
- The issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance.
- The Commissioner is hereby requested to grant an extension of time for the appropriate length of time, should one be necessary, in connection with this filing or any future filing submitted to the U.S. Patent and Trademark Office in the above-

identified application during the pendency of this application. The Commissioner is further authorized to charge any fees related to any such extension of time to deposit account 23-3050. This sheet is provided in duplicate.

SHOULD ANY DEFICIENCIES APPEAR with respect to this application, including deficiencies in payment of fees, missing parts of the application or otherwise, the United States Patent and Trademark Office is respectfully requested to promptly notify the undersigned.

Date: October 17, 2000

Michael P. Dunnam Registration No. 32,611

Woodcock Washburn Kurtz Mackiewicz & Norris LLP One Liberty Place - 46th Floor Philadelphia PA 19103 Telephone: (215) 568-3100

Facsimile: (215) 568-3439

© 1997 WWKMN

POWER CONVERTING METHOD AND APPARATUS

BACKGROUND OF THE INVENTION

The present invention relates to a method and apparatus for converting DC power to DC power of a different voltage from that of the original one.

5

10

15

25

30

Fig. 7 shows a first prior art voltage-drop type DC-DC converter 70. The DC-DC converter 70 converts an input voltage $V_{\text{\tiny I}}$ of a DC power supply 71 to an output voltage $V_{\text{\tiny O}}$ that is lower than the input voltage V_{I} . When a transistor TR is ON, a voltage V_{I} - V_{O} is applied to a coil CL. amount of change in current, AIL, when the transistor TR is turned on is expressed by $\Delta IL = \{(V_I - V_0)/L\}T_{on}$ where L is the inductance of the coil CL and T_{on} is the ON duration of the transistor TR. When the transistor TR is turned off, a commutation diode D keeps the current flowing across the coil CL. When the transistor TR is turned off, the amount of current change ΔIL is expressed by ΔIL = $(V_0/L)T_{\text{off}}$ where T_{off} is the OFF duration of the transistor TR. When the current continuously flows across the coil CL, both current changes are equal to each other in a steady state. Therefore, the output voltage V_0 is $\{T_{on}/(T_{on} + T_{off})\}V_I$, which is smaller than the input voltage V_{I} .

Other known types of DC-DC converters than the voltage-drop type DC-DC converter 70 include a booster type DC-DC converter and a booster/voltage-drop type DC-DC converter.

Recently, hybrid motor vehicles have been put to use in order to improve fuel efficiency and reduce the exhaust gas of motor vehicles. Hybrid vehicles use a running motor

when they are started or when they run at a low speed, and use an engine when they run at a middle speed. The operational voltage for various kinds of units, such as a headlight, which a hybrid motor vehicle is equipped, is lower than the operational voltage for the running motor. The conventional hybrid motor vehicles therefore need two power supplies, a high-voltage power supply for the running motor and a low-voltage power supply for the various kinds of units.

10

15 15 20

5

Fig. 8 shows a prior art high-voltage and low-voltage generating apparatus 80. The apparatus 80 has an engine 51, an alternator 52, a high-voltage battery 53, a low-voltage battery 56 and a DC-DC converter 54. The alternator 52 has a three-phase AC generator 52a and a three-phase full-wave rectifier 52b which are driven by the engine 51. A high-voltage unit (motor) 55 is connected to the high-voltage battery 53. The alternator 52 generates high-voltage DC power to charge the high-voltage battery 53. The DC-DC converter 54 lowers the voltage of the high-voltage battery 53 to charge the low-voltage battery 56 and supplies the lowered voltage to a low-voltage unit 57.

25

Fig. 9 shows another prior art high-voltage and low-voltage supplying apparatus 90. The apparatus 90 has two alternators 52 connected to the engine 51. The two alternators 52 respectively charge the high-voltage battery 53 and the low-voltage battery 56.

30

The apparatus 80 in Fig. 8 needs the large-capacity DC-DC converter 54 and two batteries 53 and 56 and thus inevitably is large and heavy. The apparatus 90 in Fig. 9 is heavy and bulky because of the two alternators 52. The apparatuses 80 and 90 in Figs. 8 and 9 are therefore unfit

for hybrid motor vehicles.

SUMMARY OF THE INVENTION

5

It is an object of the present invention to provide a compact power converting apparatus, which stably generates at least two DC voltages, a power generating method, and a vehicle having the power converting apparatus.

10

To achieve the above object, the first aspect of the present invention provides a method of supplying power using a main DC power supply for generating a predetermined voltage to supply a first output voltage substantially equal to the predetermined voltage and a second output voltage lower than the predetermined voltage. The method includes connecting a first DC power supply for generating the same voltage as the second output voltage in series to a second DC power supply for generating a differential voltage between the first output voltage and the voltage from the first DC power supply, thereby forming the main DC power supply, connecting a DC-DC converter to the second DC power supply, and stepping down the voltage output from the second DC power supply to produce the second output voltage by using the DC-DC converter.

25

The second aspect of the present invention provides a power converting apparatus for generating a first output voltage and a second output voltage lower than the first output voltage. The apparatus includes a first DC power supply for generating the same voltage as the second output voltage, a second DC power supply, which is connected in series to the first DC power supply for generating a voltage corresponding to a difference between the first output voltage and the voltage from the first DC power

30

supply, and a DC-DC converter, which is connected to the second DC power supply for converting the voltage from the second DC power supply to the second output voltage.

5

10

The third aspect of the present invention provides a method of generating a boosted voltage higher than a voltage of a main DC power supply. The method includes producing a differential voltage between a target boosted voltage and the voltage of the main DC power supply using a DC-DC converter, and producing the boosted voltage by adding the differential voltage to the voltage of the main DC power supply.

The fourth aspect of the present invention provides a power converting apparatus for generating a predetermined boosted voltage. The power converting apparatus includes a DC power supply, and a DC-DC converter, which is connected to the DC power supply, for producing a differential voltage between the predetermined boosted voltage and a voltage of the DC power supply. The predetermined boosted voltage is provided as a sum of the voltage of the DC power supply and the differential voltage.

25

30

The fifth aspect of the present invention provides a power converting method of supplying a first output voltage substantially equal to a voltage of a main battery and a second output voltage lower than the voltage of the main battery. The method includes forming the main battery by connecting a first battery for generating the same voltage as the second output voltage in series to a second battery for generating a voltage corresponding to a difference between the first output voltage and the voltage of the first battery, producing the first output voltage by adding the voltages of the first and second batteries, connecting

10

a charge power supply for generating a voltage lower than the voltage of the main battery to an output of a DC-DC converter, producing a differential voltage between the voltage of the main battery and the voltage of the charge power supply using the DC-DC converter, and charging the main battery with a sum of the differential voltage and the voltage of the charge power supply.

The sixth aspect of the present invention provides a power converting apparatus for generating a first DC voltage and a second DC voltage lower than the first DC voltage. The apparatus includes a first battery for generating the same voltage as the second DC voltage, a second battery, which is connected in series to the first battery for generating a differential voltage between the first DC voltage and the voltage of the first battery, and a polarity-inverting type DC-DC converter having an input connected to the second battery and an output connected to the first battery. The DC-DC converter includes a first switching element and a first diode connected in parallel to each other, a second switching element connected between the output of the DC-DC converter and the first battery, and a second diode connected in parallel to the second switching element.

25

30

The seventh aspect of the present invention provides a vehicle having a running motor operable with a predetermined first operational voltage, and a subload operable with a second operational voltage lower than the first operational voltage. The running motor is connected to a main battery assembly for generating the first operational voltage. The battery assembly includes a first battery cell for generating the second operational voltage and a second battery cell, connected in series to the first

30

5

10

battery cell, for generating a differential voltage between the first operational voltage and the second operational voltage. The vehicle includes a power converting apparatus, which is connected between the second battery cell and the subload, for converting the voltage of the second battery cell to the second operational voltage and supplying the second operational voltage to the subload.

Other aspects and advantages of the present invention will become apparent from the following description, taken in conjunction with the accompanying drawings, illustrating by way of example the principles of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

The features of the present invention that are believed to be novel are set forth with particularity in the appended claims. The invention, together with objects and advantages thereof, may best be understood by reference to the following description of the presently preferred embodiments together with the accompanying drawings in which:

- Fig. 1 is a schematic block diagram of a power converting apparatus according to a first embodiment of the present invention;
- Fig. 2 is a schematic block diagram of the power converting apparatus in Fig. 1, showing the flows of currents when a transistor is off;
- Fig. 3 is an waveform diagram showing a pulse signal for driving the transistor;
- Fig. 4 is a schematic block diagram of a power converting apparatus according to a second embodiment of the present invention;
 - Fig. 5 is a schematic block diagram of the power

30

5

10

converting apparatus in Fig. 4, which is being charged;

Fig. 6 is a schematic block diagram of a power converting apparatus according to a third embodiment of the present invention;

Fig. 7 is a first prior art circuit diagram of a voltage-drop type DC-DC converter;

Fig. 8 is a schematic block diagram of a prior art double power-supply system for a vehicle; and

Fig. 9 is a schematic block diagram of another prior art double power-supply system for a vehicle.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

First Embodiment

A power converting apparatus 100 according to a first embodiment of the present invention will now be described referring to Figs. 1 to 3.

As shown in Fig. 1, the power converting apparatus 100 includes a battery assembly 1 and a DC-DC converter 5.

The battery assembly 1, which is a DC power supply for a vehicular driving motor, has a first battery cell 1a and a second battery cell 1b connected in series. The first battery cell 1a generates a voltage that is the same as a desired low-voltage DC output. The second battery cell 1b generates a differential voltage between the high-voltage DC output and the output voltage of the first battery cell 1a. The voltage of the desired low-voltage DC output is substantially equal to the operational voltage (12V) for low-voltage units (e.g., a headlight 6) of a vehicle. The voltage of the high-voltage DC output is substantially equal to the operational voltage (36V) for

30

5

10

high-voltage units (e.g., a running motor 4). The battery assembly 1 has a 36-V output terminal 1d and a 12-V intermediate terminal or tap 1c. The charge voltage of the battery assembly 1 is 36V and the charge voltage at the intermediate tap 1c is 12V.

An alternator 3 includes a generator 3a and a full-wave rectifier 3b that are driven by an engine 2. While the engine 2 is running, the alternator 3 charges the battery assembly 1 with a voltage of 36V. The running motor 4 is connected to the battery assembly 1.

The DC-DC converter 5, which is a polarity-inverting type or buck boost type, is connected to the battery assembly 1. The headlight 6 is connected to the battery assembly 1 via the DC-DC converter 5. The running motor 4 is connected between the battery assembly 1 and the DC-DC converter 5.

The DC-DC converter 5 has a switching element or transistor TR1, an inductor L1, a fly-wheel diode D1, a current sensor CS1, a control circuit 7 and capacitors C1 and C2. The transistor TR1 is preferably MOSFET (Metal Oxide Semiconductor Field Effect Transistor). The transistor TR1 is connected in series to the inductor L1. That is, the transistor TR1 has a drain connected to a positive terminal 1d (36-V terminal) of the battery assembly 1 and a source connected to the inductor L1. The fly-wheel diode D1 is located between a ground terminal or 0-V terminal and a node between the transistor TR1 and the inductor L1. The capacitor C1 is located between the 36-V terminal 1d and the 12-V terminal 1c of the battery assembly 1. The capacitor C2 is located between the 12-V terminal 1c of the battery

30

5

10

The transistor TR1 and the current sensor CS1 are connected to the control circuit 7. Also connected to the control circuit 7 are a first voltage sensor 8 for detecting an output voltage of the running motor 4 and a second voltage sensor 9 for detecting an output voltage Vo of the headlight 6. The control circuit 7 controls the ON/OFF switching of the transistor TR1 with a high frequency in such a way that the ratio of the detected voltage of the first voltage sensor 8 to the detected voltage of the second voltage sensor 9 becomes 3:1. This makes it possible to keep the ratio of the output voltage to the running motor 4 to the output voltage to the headlight 6 at 3:1.

The control circuit 7 incorporates a comparator 7a and a triangular wave oscillator 7b. The comparator 7a compares the difference between the detected voltages of the first and second voltage sensors 8 and 9 and the output of the triangular wave oscillator 7b and generates a transistor drive pulse signal (see Fig. 3) according to the comparison result. The control circuit 7 supplies the transistor drive pulse signal to the transistor TR1. transistor drive pulse signal controls an ON time Ton and OFF time T_{off} of the transistor TR1. When the ratio of the ON time T_{on} of the transistor TR1 to the OFF time T_{off} is 1 : 2, for example, the ratio of the detected voltage of the first voltage sensor 8 to the detected voltage of the second voltage sensor 9 is 3 : 1. When the ratio of the detected voltages of the first and second voltage sensors 8 and 9 is shifted from 3 : 1, the control circuit 7 changes the ON time T_{on} and the OFF time T_{off} to correct for the deviation.

The operation of the power converting apparatus 100

will now be discussed.

The DC-DC converter 5 has input terminals that are the 36-V terminal 1d and the 12-V terminal 1c of the battery assembly 1, and 12V of the battery assembly 1 is a ground voltage in the DC-DC converter 5. Therefore, the input voltage $V_{\rm I}$ of the DC-DC converter 5 is 24V. The output voltage $V_{\rm O}$ of the DC-DC converter 5 is -12V because the 12-V intermediate tap 1c is taken as a reference.

10

5

When the transistor TR1 is turned on, the current flows as indicated by an arrow A in Fig. 1. This causes the inductor L1 to store the power that is supplied from the second battery cell 1b. Irrespective of the switching of the transistor TR1, the capacitor C2 is charged with the current from the first battery cell 1a and the current is supplied to the headlight 6 from the capacitor C2.

When the transistor TR1 is turned off while the current is flowing in the inductor L1, the diode D1 keeps the current flowing through the inductor L1, the fly-wheel diode D1 is set on to keep this current. Then, the power stored in the inductor L1 is supplied to the headlight 6 (an arrow C in Fig. 2) as a low-voltage DC output.

25

Therefore, the power that drives the headlight 6 is supplied from both the DC-DC converter 5 and the first battery cell 1a. When the current supplied to the headlight 6 is 100 A, for example, a current of 67 A from the DC-DC converter 5 and a current of 33 A from the battery cell 1a are both supplied to the headlight 6.

30

When the power converting apparatus 100 is in a steady state, the output voltage V_{o} and an output current

25

30

 I_0 are expressed by the following equations.

5 $I_0 = (V_I T_{on})^2 / \{2L (T_{on} + T_{off}) V_0\}$

 $V_0 = (T_{on}/T_{off}) V_I$

The control circuit 7 controls the ON time T_{on} and OFF time T_{off} in such a way as to set $V_0 = -V_I/2$, so that 2/3 of the total power supplied to the headlight 6 is supplied from the DC-DC converter 5 and the remaining 1/3 is supplied from the battery assembly 1. Specifically, the control circuit 7 monitors the output current I_0 detected by the current sensor CS1 and controls the ON time T_{on} and the OFF time T_{off} in such a manner that the ratio of the voltage supplied to the running motor 4 to the supply voltage V_0 to the headlight 6 becomes 3 : 1.

When a current of 33 A or larger is supplied from the battery cell 1a (overloaded state), for example, the ratio of the detected voltages of the voltage sensors 8 and 9 is shifted from 3:1. In this case, after the overloaded state is released, the control circuit 7 controls the ON time $T_{\rm on}$ and the OFF time $T_{\rm off}$ in such a manner that the ratio of the detected voltages of the voltage sensors 8 and 9 becomes 3:1.

If there is no variation in the load of the headlight 6, the ratio of the ON time $T_{\rm on}$ to the OFF time $T_{\rm off}$ does not change. Because the load of the headlight 6 frequently varies, however, the control circuit 7 controls the ON time $T_{\rm on}$ and the OFF time $T_{\rm off}$ based on detection signals from the first and second voltage sensors 8 and 9 in such a way that the ratio of the detected voltages of the voltage sensors 8 and 9 becomes 3:1.

25

30

5

10

As shown in Fig. 3, the voltage of the triangular wave signal of the triangular wave oscillator 7b periodically changes. The comparator 7c generates a pulse signal whose level goes to high when a comparison voltage Vc based on the difference between the detection signals of the first and second voltage sensors 8 and 9 is smaller than the voltage of the triangular wave signal and goes to low when the comparison voltage Vc is greater than the voltage of the triangular wave signal, and sends the pulse signal to the transistor TR1. The transistor TR1 is turned on when the pulse signal has a high level, and is turned off when the pulse signal has a low level. comparison voltage Vc is equal to a predetermined value Vs, the ratio of the detected voltages of both voltage sensors 8 and 9 is 3:1. At this time, the control circuit 7 outputs a pulse signal whose ratio of the ON time Ton to the OFF time T_{off} is 1:2. When the ratio of the detected voltages of both voltage sensors 8 and 9 is larger than 3:1, the detected voltage of the first voltage sensor 8 is relatively large and the comparison voltage Vc is larger than the value Vs. At this time, the ON time T_{on} is controlled to be shorter. When the ratio of the detected voltages of both voltage sensors 8 and 9 is smaller than 3:1, the comparison voltage Vc is smaller than the value Vs. At this time, the ON time $T_{\rm on}$ is controlled to be By adjusting the ON time T_{on} of the switching element using the triangular wave signal, the control circuit 7 controls the switching of the transistor TR1 in such a way that the ratio of one output voltage to the other coincides with a target value.

The capacitors C1 and C2 smooth the current from the battery assembly 1. When the capacitance of the transistor TR1 is relatively large, the capacitor C1 can be

eliminated.

The power converting apparatus 100 of the first embodiment has the following advantages.

5

The DC-DC converter 5 does not directly step down the high voltage of 36V of the battery assembly 1 to the predetermined voltage of 12V, but steps down the output voltage of 24V of the second battery cell 1b to the predetermined voltage of 12V. Since the DC-DC converter 5 needs a small capacity, the DC-DC converter 5 can be made compact, which makes the power converting apparatus 100 compact.

10

m)

th lo ou vo ta

The battery assembly 1 has the first battery cell 1a that outputs a voltage of 12V equal to the voltage of the low-voltage DC output, the second battery cell 1b that outputs a voltage of 24V or the difference between the voltage of the high-voltage DC output and the output voltage of the first battery cell 1a, and the intermediate tap 1c. It is therefore possible to easily secure the layout space for the first and second battery cells 1a and 1b.

25

19515 15 20

As the input terminal of the DC-DC converter 5 is connected to the second battery cell 1b and the output terminal to the first battery cell 1a, the power converting apparatus 100 has a simple structure.

30

Being compact and simple in structure, the power converting apparatus 100 is suitable for use in a vehicle.

Because a part of the output of the DC-DC converter 5 is used to charge the first battery cell 1a when the

30

5

10

discharge capacity of the first battery cell 1a drops down to or below a predetermined value, discharging the first battery cell 1a alone is suppressed.

Second Embodiment

A power converting apparatus 110 according to a second embodiment of this invention will now be described referring to Figs. 4 and 5. The power converting apparatus 110 steps down or boosts the supply voltage. The power converting apparatus 110 has a second transistor (MOSFET) TR2 in place of the fly-wheel diode D1 used in the first embodiment.

Each of the first and second transistors TR1 and TR2 has a parasitic diode between its source and drain as indicated by dotted lines in Fig. 4. Therefore, the use of MOSFETs for the first transistor TR1 (switching element) and the second transistor TR2 is equivalent to the use of a parallel circuit of a switching element and a diode. When the second transistor TR2 is kept off, the DC-DC converter 5 functions the same as the first embodiment.

In the power converting apparatus 110, therefore, the second transistor TR2 is normally kept off and the first transistor TR1 is switched on or off. When the output voltage of the battery assembly 1 falls below a predetermined voltage, an additional DC power supply 10 is connected to the output terminal of the DC-DC converter 5 as shown in Fig. 5 to charge the battery assembly 1. The additional DC power supply 10 can have the same output voltage as the output voltage of the first battery cell 1a.

At the time of charging the battery assembly 1, the

first transistor TR1 is kept off and the second transistor TR2 is switched on and off. In this case, the DC-DC converter 5 serves as a booster type DC-DC converter.

10

5

15

25

30

The output voltage of the DC power supply 10 or the input voltage to the DC-DC converter 5 in a boost mode is expressed by V_12 and the output voltage of the DC-DC converter 5 is expressed by V_02 . The voltage that is applied to the inductor L1 when the second transistor TR2 is on is V_12 while the voltage that is applied to the inductor L1 when the second transistor TR2 is off is (V_02 - V_12). When the current continuously flows across the inductor L1, therefore, the amount of a change in the current flowing across the inductor L1 during the ON time T_{on} is substantially equal to the amount of a change in the current flowing across the inductor L1 during the OFF time T_{off} . This is shown in the following equation.

 $(V_I2/L)T_{on} = \{(V_02 - V_I2)/L\}T_{off}$

Thus, $V_02 = \{ (T_{on} + T_{off}) / T_{off} \} V_12$.

of the DC-DC converter 5.

of the second transistor TR2 in such a way that the ratio of V_02 to V_12 becomes 2:1. In other words, the control circuit 7 controls the second transistor TR2 in such a way that the ratio of the difference (24V) between the charge voltage of 36V of the battery assembly 1 and the output voltage of 12V of the DC power supply 10 to the output voltage of 12V of the DC power supply 10 is maintained at 2:1. As a result, the battery assembly 1 is charged with a voltage of 36V, which is the output voltage of 12V of the DC power supply 10 plus the boosted output voltage of 24V

The control circuit 7 controls the ON/OFF switching

30

The second embodiment therefore has the advantages of the first embodiment and the following additional advantages.

5

10

The DC-DC converter 5 outputs a voltage that is the output voltage (12V) of the DC power supply 10 subtracted from the charge voltage (36V) of the battery assembly 1, and this output voltage (24V) is added to the output voltage (12V) of the DC power supply 10. The battery assembly 1 can be charged with the resultant voltage (36V). This means that the battery assembly 1 can be charged using the battery installed in another vehicle which has only the conventional battery for low-voltage units.

The power converting apparatus 110 can be constructed by providing the polarity-inverting type DC-DC converter 5 with the transistor TR1, which has a switching element and a diode connected in parallel, and the transistor TR2, which also has a switching element and a diode connected in parallel and which is used in place of the fly-wheel diode D1.

The transistors (MOSFETs) TR1 and TR2 serve as diodes when they are turned off in a step-down mode or a boost Therefore, the structure of the power converting apparatus 110 is simpler than that of the power converting apparatus that has a parallel circuit of a switch element and a diode.

Third Embodiment

A power converting apparatus 120 according to a third embodiment of this invention will now be described referring to Fig. 6. The power converting apparatus 120

has an insulated DC-DC converter (fly-back converter) 13 that has a transformer capability.

As shown in Fig. 6, the fly-back converter 13 is connected to the second battery cell 1b, so that the output voltage (24V) of the second battery cell 1b is applied to the fly-back converter 13. The headlight 6 is connected to the output terminal of the fly-back converter 13 and the first battery cell 1a.

10

5

When a transistor TR is on, electric energy is stored in a transformer T. When the transistor TR is off, on the other hand, the electric energy stored in the transformer T is discharged. Given that the number of turns of the primary winding of the transformer T is denoted by n1 and the number of turns of the secondary winding is denoted by n2, the following equation is satisfied when the secondary current continuously flows.

 $V_0 = (n2/n1) (T_{on}/T_{off}) V_I$

The control circuit 7 controls the ON/OFF action of the transistor TR in such a way that the output voltage $V_{\rm 0}$ of the fly-back converter 13 coincides with the operational voltage of 12V for the headlight 6.

The third embodiment has the following additional advantages.

30

25

The use of the insulated DC-DC converter 5 permits the power converting apparatus 120 to be used as a switching power supply that must provide electric insulation between the input side device and the output side device.

10

As the power converting apparatus 120 has the insulated fly-back converter 13, it has a simpler structure than one that has a forward converter.

The first to third embodiments may be modified as follows.

Instead of using the battery assembly 1 that has the first battery cell 1a and the second battery cell 1b, the first battery cell 1a and the second battery cell 1b may be arranged separately.

In the first to third embodiments, the control circuit 7 that performs analog control of the ratio of the ON time of the transistor TR1 to the OFF time thereof may be replaced with a control unit that has a CPU. In this case, the CPU computes the ratio of the ON time of the transistor TR1 to the OFF time thereof based on the detection signals of the first and second voltage sensors 8 and 9. Preferably, the ON/OFF switching of the transistor TR1 is controlled using PWM based on this computed ratio. Available as this CPU is a CPU that is used in an apparatus other than the power converting apparatus 100, 110 or 120.

25

respectively detect the amount of the current flowing across the inductor L1 and the amount of the current coming back to the battery cell 1a from the headlight 6. In this case, the control circuit 7 controls the transistor TR1 in such a manner that the ratio of the value of the two currents detected by the current sensors becomes a predetermined value (e.g., 2:1). In the second and third embodiments, the currents may be detected instead of the

The first embodiment may use current sensors which

30

voltages. In this case too, the control circuit 7 controls

the transistor based on the detected current values.

In the second embodiment, a parallel circuit of a bipolar transistor and a diode may be provided in place of the transistors (MOSFETs) TR1 and TR2.

Instead of the MOSFET and the bipolar transistor, for example, other switching elements, such as an SIT (Static Induction Transistor) and a thyristor, may be used.

Instead of the running motor 4, another unit may be connected to the power converting apparatus 100, 110 or 120. The power converting apparatus 100, 110 or 120 may be adapted for use in a vehicle that is not equipped with the running motor 4 or may be adapted for use in a battery-powered vehicle which does not have an engine.

The power converting apparatus 100, 110 or 120 may be adapted for use in other apparatuses and equipment than a vehicle.

The fly-back converter 13 may be replaced with another type of DC-DC converter which has the transformer T .

A booster type insulated converter whose transformer T has a different turn ratio of the primary winding to the secondary winding from that of the transformer T of the fly-back converter 13 may be used.

It should be apparent to those skilled in the art that the present invention may be embodied in many other specific forms without departing from the spirit or scope of the invention. Therefore, the present examples and

10

5

30

25

embodiments are to be considered as illustrative and not restrictive and the invention is not to be limited to the details given herein, but may be modified within the scope and equivalence of the appended claims.

10

25

30

What is claimed is:

A method of supplying power using a main DC power supply for generating a predetermined voltage to supply a first output voltage substantially equal to the predetermined voltage and a second output voltage lower than the predetermined voltage, comprising the steps of:

connecting a first DC power supply for generating the same voltage as the second output voltage in series to a second DC power supply for generating a differential voltage between the first output voltage and the voltage from the first DC power supply, thereby forming the main DC power supply;

connecting a DC-DC converter to the second DC power supply; and

stepping down the voltage output from the second DC power supply to produce the second output voltage by using the DC-DC converter.

- A power converting apparatus for generating a first output voltage and a second output voltage lower than the first output voltage, comprising:
- a first DC power supply for generating the same voltage as the second output voltage;
- a second DC power supply, connected in series to the first DC power supply, for generating a voltage corresponding to a difference between the first output voltage and the voltage from the first DC power supply; and
- a DC-DC converter, connected to the second DC power supply, for converting the voltage from the second DC power supply to the second output voltage.
- The power converting apparatus according to claim 2, wherein the DC-DC converter includes a polarity-

inverting type DC-DC converter, the second DC power supply is connected to an input of the polarity-inverting type DC-DC converter, and the first DC power supply is connected to an output of the DC-DC converter.

5

4. The power converting apparatus according to claim 2, wherein the DC-DC converter includes an insulated DC-DC converter, the second DC power supply is connected to an input of the insulated DC-DC converter, and the first DC power supply is connected to an output of the DC-DC converter.

10

5. A method of generating a boosted voltage higher than a voltage of a main DC power supply, comprising the steps of:

producing a differential voltage between a target boosted voltage and the voltage of the main DC power supply using a DC-DC converter; and

producing the boosted voltage by adding the differential voltage to the voltage of the main DC power supply.

25

6. A power converting apparatus for generating a predetermined boosted voltage, comprising:

a DC power supply; and

a DC-DC converter, connected to the DC power supply, for producing a differential voltage between the predetermined boosted voltage and a voltage of the DC power supply, wherein the predetermined boosted voltage is provided as a sum of the voltage of the DC power supply and the differential voltage.

30

7. A power converting method of supplying a first output voltage substantially equal to a voltage of a main

15 15 20

25

30

5

10

battery and a second output voltage lower than the voltage of the main battery, comprising the steps of:

forming the main battery by connecting a first battery for generating the same voltage as the second output voltage in series to a second battery for generating a voltage corresponding to a difference between the first output voltage and the voltage of the first battery;

producing the first output voltage by adding the voltages of the first and second batteries;

connecting a charge power supply for generating a voltage lower than the voltage of the main battery to an output of a DC-DC converter;

producing a differential voltage between the voltage of the main battery and the voltage of the charge power supply using the DC-DC converter; and

charging the main battery with a sum of the differential voltage and the voltage of the charge power supply.

8. A power converting apparatus for generating a first DC voltage and a second DC voltage lower than the first DC voltage, comprising:

a first battery for generating the same voltage as the second DC voltage;

a second battery, connected in series to the first battery, for generating a differential voltage between the first DC voltage and the voltage of the first battery; and

a polarity-inverting type DC-DC converter having an input connected to the second battery and an output connected to the first battery, the DC-DC converter including a first switching element and a first diode connected in parallel to each other, a second switching element connected between the output of the DC-DC converter and the first battery, and a second diode connected in

10

9. The power converting apparatus according to claim 8, wherein the first switching element and the first diode are a first MOSFET and the second switching element and the second diode are a second MOSFET.

10. A vehicle comprising:

a running motor operable with a predetermined first operational voltage;

a subload operable with a second operational voltage lower than the first operational voltage;

a main battery assembly, connected to the running motor, for generating the first operational voltage, the battery assembly including a first battery cell for generating the second operational voltage and a second battery cell, connected in series to the first battery cell, for generating a differential voltage between the first operational voltage and the second operational voltage; and

a power converting apparatus, connected between the second battery cell and the subload, for converting the voltage of the second battery cell to the second operational voltage and supplying the second operational voltage to the subload.

11. A power converting apparatus for a motor driven vehicle, comprising:

a main battery assembly, connected between a highpotential power supply and a low-potential power supply,
for generating a main output voltage for driving the
vehicle motor, the main battery assembly including a first
battery cell for generating a first voltage lower than the
main output voltage, and a second battery cell, connected

30

25

30

5

10

in series to the first battery cell, for generating a second voltage corresponding to a difference between the main output voltage and the first voltage; and

a DC-DC converter, connected to the second battery cell, for converting the second voltage to a low voltage substantially equal to the first voltage.

- 12. The power converting apparatus according to claim 11, further comprising:
- a first voltage sensor for detecting the main output voltage, and
- a second voltage sensor for detecting the low voltage; and

wherein the DC-DC converter includes:

a switching element responsive to a control signal; an inductance connected in series to the switching element; and

a control circuit, connected to the switching element and the first and second voltage sensors, for supplying the switching element with the control signal for controlling ON and OFF actions of the switching element based on detection signals from the first and second voltage sensors.

- 13. The power converting apparatus according to claim 12, wherein the switching element includes a MOSFET.
- 14. The power converting apparatus according to claim 12, wherein the control circuit includes:

a triangular wave oscillator for generating a triangular wave signal having a predetermined cycle; and

a comparator for comparing a difference between detection signals from the first and second voltage sensors with the triangular wave signal and generating a pulse

25

30

- The power converting apparatus according to claim 14, wherein the control circuit controls a ratio of an ON time of the switching element to an OFF time thereof by changing a pulse width of the pulse signal, thereby adjusting a level of the low voltage.
- 16. The power converting apparatus according to claim 14, wherein the comparator generates a high-level pulse signal when the difference between the detection signals from the first and second voltage sensors is greater than the triangular wave signal and generates a low-level pulse signal when the difference between the detection signals from the first and second voltage sensors is smaller than the triangular wave signal.
- A vehicle that os driven using at least electric power, comprising:

a motor for running the vehicle, the motor being operable with a predetermined first operational voltage;

an ancillary unit operable with a second operational voltage lower than the first operational voltage; and

a main battery assembly, connected to the motor, for generating the first operational voltage, the main battery assembly including,

a first battery cell for generating the second operational voltage,

a second battery cell, connected in series to the first battery cell, for generating a voltage corresponding to a difference between the first operational voltage and the second operational voltage, and

a DC-DC converter, connected between the second battery cell and the ancillary unit, for converting the voltage generated by the second battery cell to the second operational voltage.

5

18. The vehicle according to claim 17, further comprising:

an engine for driving the vehicle in complementary and cooperation with the motor; and

10

an alternator, connected to the engine and the main battery assembly, for generating electric energy from drive power of the engine and charging the main battery assembly with the electric energy.

10

ABSTRACT OF THE INVENTION

A power converter that can be used in a vehicle equipped with an electrical motor provides power to both the electrical motor and other, auxiliary loads, such as a headlamp, that require a different voltage. The power converter includes a first DC power supply that generates a first voltage equivalent to the voltage required by the auxiliary loads and a second DC power supply, connected in series with the first DC power supply, that generates a differential voltage, where the sum of the differential voltage and the first voltage is the voltage required to drive the electrical motor. A DC-DC converter is connected to the second power supply and converts the differential voltage to the voltage required to drive the auxiliary loads.

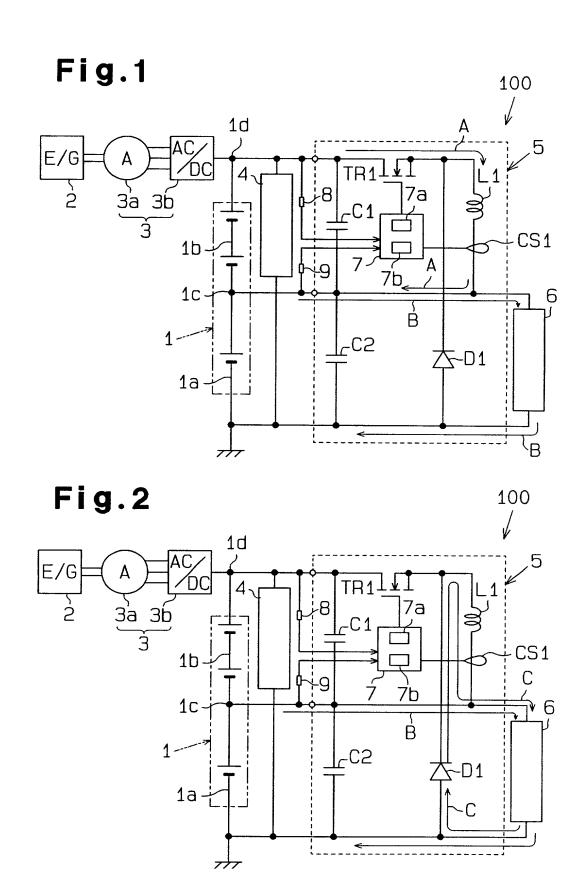
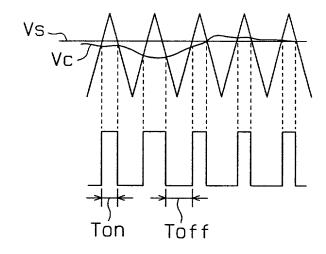
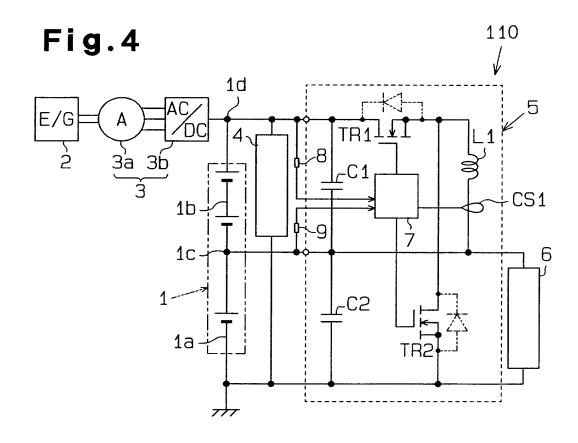
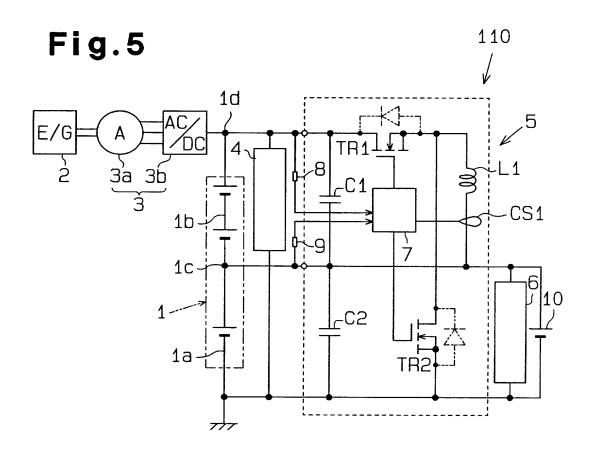


Fig.3







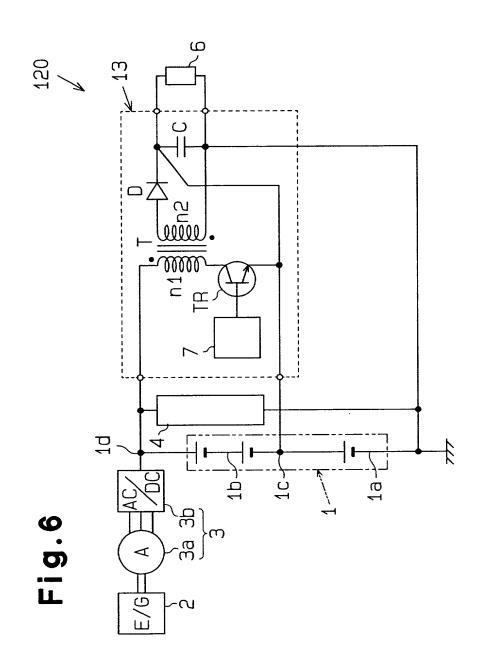


Fig.7 (Prior Art)

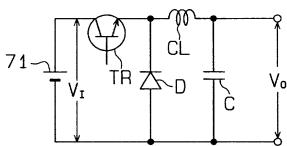


Fig.8 (Prior Art)

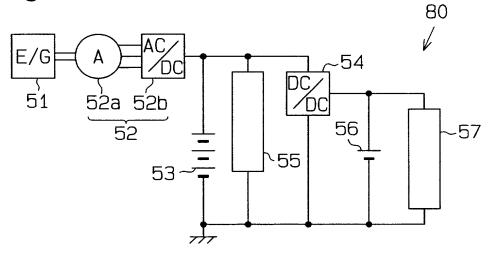
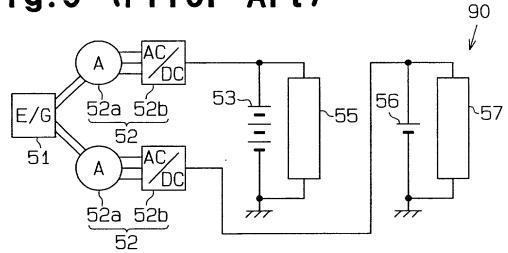


Fig.9 (Prior Art)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of	f:				
		Group Art	Unit:	Not ÿ et	assigned.
Tadayoshi KACHI, Seiki SAKATA For:	Junichi TAKEUCHI, and	Examiner:	Not :	yet assig	ned.
POWER CONVERTING	METHOD AND APPARATUS				
I	DECLARATION AND POV	VER OF ATTORM	NEY		
As a below named in	ventor, I hereby declare that:				
My residence, post of	ffice address and citizenship a	are as stated below:	next to	my name;	and
	e original, first and sole inven nt inventor (if plural names ar nich a				
X	Utility Patent	Design Patent			
is sought on the inve	ntion, whose title appears abo	ve, the specification	n of wł	nich:	
X	is attached hereto.				
	was filed on	as Serial No			·
	said application having beer				
•	ave reviewed and understand ng the claims, as amended by				
•	ity to disclose to the U.S. Pate to the patentability of this ap				

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a-d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below

1.56.

any foreign application for patent or inventor's certificate having a filing date before that of any application on which priority is claimed:

Priority Claimed (If X'd)	Country	Serial Number	Date Filed
X	Japan	Pat. Appln. No. 11-3	12402 02 / 11 / 1999
		Who are the second seco	
disclosed i of 35 U.S. Office all i which beca	n the prior United States ap C. § 112, I acknowledge th nformation known to be m	oplication in the mann e duty to disclose to the aterial to patentability filing date of the prior	ns of this application is not her provided by the first paragraph he U.S. Patent and Trademark as defined in 37 CFR § 1.56 application and the national or
	Serial Number	Date Filed	Patented/Pending/Abandoned

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

S	erial Number	Date Filed	
			_
			OODCOCK WASHBURN KURTZ
MACKIEWI	ICZ & NORRIS LLP,	, One Liberty Place	- 46th Floor, Philadelphia,
	. 19103 as attorney(s) as the Patent and Traden		osecute this application and to transact ted therewith:

Steven J. Rocci	Reg. No. <u>30,489</u>	
Michael P. Dunnam	Reg. No. 32,611	

Address all telephone calls and correspondence to the first-listed attorney of record at:

WOODCOCK WASHBURN KURTZ MACKIEWICZ & NORRIS LLP

One Liberty Place - 46th Floor Philadelphia PA 19103

Telephone No.: (215) 568-3100 Facsimile No.: (215) 568-3439

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name Tadayoshi KACHI	Tadayoshi Kachi
c/o KABUSHIKI KAISHA TOYODA JIDOSHOKKI SEISAKUSHO 2-1, Toyoda-cho, Kariya-shi, Aichi-ken, Japan	Signature Date of Signature: September 21, 2000
Mailing Address	
	Citizenship:
Kariya-shi, Japan	
City/State of Actual Residence	

Name Junichi TAKEUCHI	Junichi Takeuchi Signature
c/o KABUSHIKI KAISHA TOYODA JIDOSHOKKI SEISAKUSHO 2-1, Toyoda-cho, Kariya-shi, Aichi-ken, Japan Mailing Address	Date of Signature: September 21, 2000
Kariya-shi, Japan City/State of Actual Residence	Citizenship: Japan

Name Seiki SAKATA	Seīkī Sakata
c/o KABUSHIKI KAISHA TOYODA JIDOSHOKKI SEISAKUSHO 2-1, Toyoda-cho, Kariya-shi, Aichi-ken, Japan	Signature Date of Signature: September 21, 2000
Mailing Address	
	Citizenship: Japan
Kariya-shi, Japan City/State of Actual Residence	

Group Art Unit: Not yet

Examiner: Not yet assigned.

assigned.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Takayoshi Kachi, Junichi Takeuchi and Seiki Sakata

Serial No.: Not yet assigned.

Filed: Herewith.

For: Power Converting Method and

Apparatus

Assistant Commissioner for Patents Washington DC 20231

Sir:

ASSOCIATE POWER OF ATTORNEY

The undersigned, of the firm WOODCOCK WASHBURN KURTZ MACKIEWICZ & NORRIS LLP, One Liberty Place - 46th Floor, Philadelphia, Pennsylvania 19103, Attorney and/or Agents for Applicant(s), hereby appoints the following:

Robert B. Washburn	Registration No. 16,574	Lynn A. Malinoski	Registration No. 38,788
Richard E. Kurtz	Registration No. 19,263	Michael J. Swope	Registration No. 38,041
John J. Mackiewicz	Registration No. 19,709	Michael J. Bonella	Registration No. 41,628
Norman L. Norris	Registration No. 24,196	Harold H. Fullmer	Registration No. 42,560
Dale M. Heist	Registration No. 28,425	William R. Richter	Registration No. 43,879
John W. Caldwell	Registration No. 28,937	John E. McGlynn	Registration No. 42,863
Gary H. Levin	Registration No. 28,734	Jonathan M. Waldman	Registration No. 40,861
Steven J. Rocci	Registration No. 30,489	Chad Ziegler	Registration No. 44,273
Dianne B. Elderkin	Registration No. 28,598	Gwilym J.O. Attwell	Registration No. 45,449
John P. Donohue, Jr.	Registration No. 29,916	David N. Farsiou	Registration No. 44,104
Henrik D. Parker	Registration No. 31,863	Paul K. Legaard	Registration No. 38,534
Suzanne E. Miller	Registration No. 32,279	Maureen S. Gibbons	Registration No. 44,121
Lynn B. Morreale	Registration No. 32,842	Steven H. Meyer	Registration No. 37,189
Mark DeLuca	Registration No. 33,229	Paul B. Milcetic	Registration No. P46,261
Joseph Lucci	Registration No. 33,307	Joseph R. Condo	Registration No. 42,431
Michael D. Stein	Registration No. 34,734	Michael K. Jones	Registration No. 41,100
Albert J. Marcellino	Registration No. 34,664	Frank T. Carroll	Registration No. 42,392
David R. Bailey	Registration No. 35,057	Mark J. Rosen	Registration No. 39,822
Doreen Yatko Trujillo	Registration No. 35,719	Mitchell R. Brustein	Registration No. 38,394
Barbara L. Mullin	Registration No. 38,250	Eric H. Vance	Registration No. 47,151
Kevin M. Flannery	Registration No. 35,871	Peter M. Ullman	Registration No. 43,963
Michael P. Straher	Registration No. 38,325	Thomas E. Watson	Registration No. 43,243
David A. Cherry	Registration No. 35,099	Richard B. LeBlanc	Registration No. 39,495
Terence P. Strobaugh	Registration No. 25,460	Joseph D. Rossi	Registration No. 47,039
-			

DOCKET NO. TALW-0152 -2- PATENT

George J. Awad	Registration No. 46,528	Christine A. Goddard	Registration No. 46,731
Steven D. Maslowski	Registration No. 46,905	Gregory L. Hillyer	Registration No. 44,154
S. Maurice Valla	Registration No. 43,966	Patrick J. Farley	Registration No. 42,524
Vincent J. Roccia	Registration No. 43,887	Ellen M. Klann	Registration No. 44,836
Robin S. Quartin	Registration No. 45,028	Steven B. Samuels	Registration No. 37,711
Maria M. Kourtakis	Registration No. 41.126	Susan C. Murphy	Registration No. 46,221

his/her associates with full power to prosecute the above-identified application and to transact all business in the Patent Office connected therewith and requests that correspondence continue to be directed to the firm of WOODCOCK WASHBURN KURTZ MACKIEWICZ & NORRIS LLP at the above address.

Date: October 17, 2000

Michael P. Dunnam Registration No. 32,611

Woodcock Washburn Kurtz Mackiewicz & Norris LLP One Liberty Place - 46th Floor Philadelphia PA 19103 Telephone: (215) 568-3100

Facsimile: (215) 568-3439

© 1997 WWKMN